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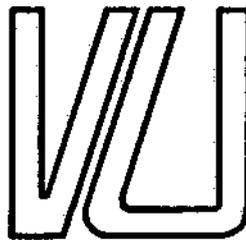
# **SERIE RESEARCH MEMORANDA**

MONOTONICITY OF PERFORMANCE MEASURES  
IN A PROCESSOR SHARING QUEUE

P.R. de Waal  
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**VRIJE UNIVERSITEIT**  
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**A M S T E R D A M**



# Monotonicity of Performance Measures in a Processor Sharing Queue

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In this paper we study the monotonicity of performance measures in a processor sharing queue with two types of customers. The access control law is such that when a new customer arrives he is admitted only if the number of customers of the same type that is already present in the queue does not exceed a predefined critical level. We show that performance measures such as throughput, mean queue length and mean sojourn time are monotonic functions of the critical level for one type if the critical level for the other type is held constant. Monotonicity of throughput and mean queue length is proven by comparing policies for discrete time Markov processes. Monotonicity of the mean sojourn time is proven directly with the closed form formula for this measure, using the existence of a product-form equilibrium distribution for the queueing system.

1980 Mathematics Subject Classification: 60K25, 68M20, 90B22.

Key Words & Phrases: monotonicity, processor sharing.

## 1. INTRODUCTION.

During the last few years a number of papers has been published about monotonicity of performance measures in queueing systems. These results are used e.g. to provide structural properties of the queueing systems or bounds on performance measures of analytically untractable queueing systems. The monotonicity results as reported in this paper stem from a study of an optimal control problem for a processor sharing queue. Monotonicity of performance measures here provides necessary and sufficient conditions for the existence of optimal control laws. Furthermore it also suggests an efficient algorithm for finding the optimal control law (cf. De Waal [23]). The techniques that have been used for establishing monotonicity results can roughly be divided into four classes.

The first approach is based upon preservation of monotonicity of one-step transition operators (cf. Stoyan [21]). In analogy with Van Dijk and Van der Wal [11, Remark 5.1.] one easily finds a counterexample of this preservation under processor sharing disciplines, so this method does not apply.

In the second class of papers monotonicity results are proven for queueing systems that have product-form equilibrium distributions. The closed form formulas of the performance measures that can be derived from these distributions, are subsequently used to prove the desired results. In general the proofs are very technical and lack any probabilistic interpretation. Examples of papers are Yao [25], Robertazzi and Lazar [15], Suri [22], Shanthikumar and Yao [17, 20] and Van Doremalen and De Waal [12].

In the third class of papers monotonicity results are established by stochastic coupling and sample path arguments. In these papers inequalities for the throughput of two related queueing systems are proven by comparing realisations of the arrival or departure processes in these two queueing systems. The inequalities are therefore proven for stochastic variables (i.e. the number of departed customers in a time interval). The drawback of this method is that it relies on the assumption that if the same realisation of the arrival process is fed into both queueing systems, the order in which customers are served is the same for both systems. This means that overtaking of customers is not allowed, thus prohibiting the use of this technique in queueing networks with a general routing mechanism or last-come-first-served or processor sharing disciplines. The advantage of the method, when compared to the second technique, is that its use is not restricted to product-form queueing systems, thus allowing e.g. blocking. Examples of this

approach can be found in Shanthikumar and Yao [18, 19], Adan and Van der Wal [1, 2, 3] and Van Dijk, Tsoucas and Walrand [10].

In the fourth category of papers the performance measures are considered as time average rewards for Markov processes. Monotonicity of the measures is then established by proving inequalities for expected rewards over a finite horizon in the discrete time version of these processes. The inequalities thus concern real numbers, viz. expectations of random variables, as opposed to inequalities for random variables as in the second category. Examples are Van Dijk and Lamond [9], Van Dijk [7, 8], Van der Wal [24], Adan and Van der Wal [4] and Van Dijk and Van der Wal [11].

So far, however, no monotonicity results have been reported for systems with processor sharing disciplines. When comparing sample paths for such systems, there are two essential difficulties. Firstly, changing the admission policy for a processor sharing queue may lead to a change in the order in which customers are served. Secondly there is an interference between the service capacities that are awarded to the different customer types. To this end, the fourth approach will be applied and shown to be successful under natural conditions. The monotonicity results are proven by establishing bounds on the differences of the finite horizon expected rewards. The results are new in the sense that the bounds depend explicitly on the customer's type (cf. Lemma 4.3), and therefore the method itself is of interest.

This paper is organised as follows. In Section 2 the queueing system and its performance measures are introduced. The service discipline is defined as a generalisation of the standard processor sharing discipline. The queueing process, which is a continuous time Markov process, is transformed into an equivalent discrete time process in Section 3. For this discrete time process monotonicity of the throughput and mean queue length is proven in Sections 4 and 5 respectively. Monotonicity of the mean sojourn time is shown in Section 6.

## 2. INTRODUCTION OF THE QUEUEING SYSTEM AND ITS PERFORMANCE MEASURES.

Consider the queueing system in Figure 1, where two types of customers arrive according to two independent Poisson processes with arrival rates  $\lambda_1$  and  $\lambda_2$  respectively. If  $m$  customers are present of type 1 and  $n$  customers of type 2, then the population vector of the queue is said to be  $(m, n)$ ,  $m, n \in \mathbf{N}$ . Admission of new customers is described by a control law  $U: \mathbf{N}^2 \rightarrow [0, 1]^2$ , where  $U_i(m, n)$  denotes the probability that a new customer of type  $i$  is admitted if the population vector at the moment of arrival is  $(m, n)$ . Non-admitted customers are assumed to be lost. In the sequel of this paper we will restrict attention to control laws  $U$  that use only partial state information, i.e.  $U_1(m, n) = U_1(m)$ ,  $U_2(m, n) = U_2(n)$ . Furthermore the control law is restricted to be of the *critical level* type, i.e.  $U_1(m, n)$  and  $U_2(m, n)$  are of the form

$$U_1(m, n) = 1_{(m < M)} \quad (2.1)$$

$$U_2(m, n) = 1_{(n < N)}, \quad (2.2)$$

for some  $M, N \in \mathbf{N} \cup \{\infty\}$ . The control law  $U$  as defined in (2.1) and (2.2) will be referred to as  $U^{M,N}$ . The parameters of the control law,  $M$  and  $N$ , are referred to as the critical levels for type 1 and type 2 respectively. These admission policies make the arrival rates for admitted customers state-dependent, i.e. if the population vector of the queue is  $(m, n)$  then the arrival rates of type 1 and 2 are  $\lambda_1(m, n) = \lambda_1 1_{(m < M)}$  and  $\lambda_2(m, n) = \lambda_2 1_{(n < N)}$  respectively.

The service requirements for customers of type  $i$  are assumed to be exponentially distributed with service rate  $\mu_i$ ,  $i = 1, 2$ . The server is working according to the processor sharing discipline in the sense that at any time each customer of one type receives the same amount of service as any other customer of that type. The speed at which service demands of customers of both types are handled is modelled by two capacity allocation functions  $\phi_1, \phi_2: \mathbf{N}^2 \rightarrow [0, 1]$ . For each  $m, n \in \mathbf{N}$ ,  $\phi_i(m, n)$  denotes the speed at which all customers of type  $i$  together are served, so  $\mu_i \phi_i(m, n)$  is the actual service rate for type  $i$  if the population vector is  $(m, n)$ . The actual service rate for one customer of type 1 is then  $\phi_1(m, n)/m$ .

Let  $X: \Omega \times \mathbf{R}_+ \rightarrow \mathbf{N}^2$  denote the queueing process, for some appropriately chosen sample space  $\Omega$ . Performance measures for this queueing system are defined as expected time-average rewards for suitably chosen reward functions. Let  $r: \mathbf{N}^2 \rightarrow \mathbf{R}_+$  be a reward function: when the population vector is

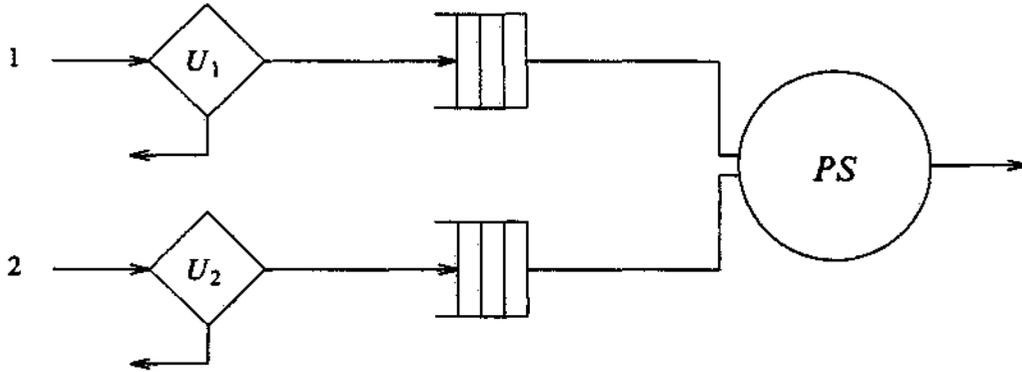


FIGURE 1. The model of the processor sharing queue.

$(m, n)$ , a reward  $r(m, n)$  is gained every time unit. A performance measure is defined as the expected time-average reward if the reward function is  $r$ :

$$\lim_{t \rightarrow \infty} \overline{E}^{U^{m,n}} \left\{ t^{-1} \int_0^t r(X_s) ds \right\},$$

where the superscript  $U$  on the expectation operator denotes its dependency of the control law that is used. Most standard performance measures can be expressed in this manner, e.g. the throughput of type  $i$  by choosing  $r(m, n) = \mu_i \phi_i(m, n)$ , and the total mean queue length by choosing  $r(m, n) = m + n$ .

### 3. TRANSFORMATION TO DISCRETE TIME.

In this section the queueing system, as introduced in the previous section, will be transformed into a discrete time setting. This formulation will become useful in later sections, since it makes times between transitions of the queueing process constant, at the cost of introducing so-called *dummy transitions*. The transformation proceeds as follows (cf. [16, 14]).

Assume that the sum of all transitions rates, i.e.  $\lambda_1 + \lambda_2 + \mu_1 + \mu_2$ , is finite. Furthermore assume that this sum is equal to one. This is no restriction, since it can be established by appropriately scaling the time axis. Introduce  $N : \Omega \times \mathbb{R}_+ \rightarrow \mathbb{N}$  a Poisson process with stochastic intensity one, and the stopping times  $t_n : \Omega \rightarrow \mathbb{R}$  as

$$t_n = \inf \{ t \mid N_t \geq n \}, \quad n \in \mathbb{N}.$$

Let  $Y : \Omega \times \mathbb{N} \rightarrow \mathbb{N}^2$  be defined as  $Y_n := X_{t_n}$ , then  $Y$  is a discrete time Markov process with transition probabilities (depending on the control law) given as:

$$p^{U^{m,n}}((m', n'); (m, n)) = \mathbf{P} \{ Y_{k+1} = (m', n') \mid Y_k = (m, n) \}$$

$$= \begin{cases} \lambda_1 I_{(m < M)} & \text{if } (m', n') = (m+1, n) \\ \lambda_2 I_{(n < N)} & \text{if } (m', n') = (m, n+1) \\ \mu_1 \phi_1(m, n) & \text{if } (m', n') = (m-1, n) \\ \mu_2 \phi_2(m, n) & \text{if } (m', n') = (m, n-1) \\ 1 - \lambda_1 I_{(m < M)} - \lambda_2 I_{(n < N)} - \mu_1 \phi_1(m, n) - \mu_2 \phi_2(m, n) & \text{if } (m', n') = (m, n). \end{cases} \quad (3.1)$$

From (3.1) we see that by transforming the continuous time process into a discrete time one, we introduce dummy transitions, i.e. transitions that do not change the state.

If we choose the one step reward for process  $Y$  equal to  $r(m, n)$ , then the expected time-average rewards for both  $Y$  and  $X$  are equal, i.e.

$$\overline{\lim}_{t \rightarrow \infty} E^U \left\{ t^{-1} \int_0^t r(X_s) ds \right\} = \overline{\lim}_{k \rightarrow \infty} E^U \left\{ k^{-1} \sum_{n=0}^{k-1} r(Y_n) \right\}.$$

#### 4. MONOTONICITY OF THE THROUGHPUT FUNCTION.

In this section we show that the throughput of customers is monotonic in the critical levels: the throughput of one type increases if the critical level for that type is increased, and it decreases if the critical level for the other type is increased.

Let the reward function be  $r(m, n) = \mu_1 \phi_1(m, n)$ , hence the performance measure under consideration is the throughput of type 1 customers. Let  $T_1^{M, N}$  and  $T_1^{M+1, N}$  denote the throughput of type 1 if the control laws  $U^{M, N}$  and  $U^{M+1, N}$  are used, respectively. Throughout the remaining sections of this paper we assume the following.

##### ASSUMPTION 4.1.

- $\phi_1(m, n)$  is non-decreasing in  $m$  and non-increasing in  $n$ .
- $\phi_2(m, n)$  is non-increasing in  $m$  and non-decreasing in  $n$ .

These assumptions are satisfied for example by the normal processor sharing service discipline, i.e.  $\phi_1(m, n) = 1_{(m+n>0)} m / (m+n)$ .

The main result of this section is the following theorem. It states the intuitively obvious monotonicity for the throughput function.

**THEOREM 4.2.** *If Assumption 4.1 holds and if*

$$\phi_1(m+1, n) + \phi_2(m+1, n) \geq \phi_1(m, n) + \phi_2(m, n) \quad (4.1a)$$

$$\phi_1(m, n+1) + \phi_2(m, n+1) \geq \phi_1(m, n) + \phi_2(m, n) \quad (4.1b)$$

then  $T_1^{M, N} \leq T_1^{M+1, N}$ ,  $M, N \in \mathbf{N}$ .

Before proceeding with the proof of Theorem 4.2. we need the following definitions. Let  $V_{M, N}^k(m, n)$  denote the total expected reward over  $k$  steps when starting in state  $(m, n)$ , for the policy  $U^{M, N}$ :

$$V_{M, N}^k(m, n) = E^{U^{M, N}} \left[ \sum_{i=0}^{k-1} r(Y_i) \mid Y_0 = (m, n) \right]. \quad (4.2)$$

Since for finite  $M$  and  $N$  the state  $(0, 0)$  is positive recurrent, the Markov chain  $Y$  is irreducible. According to the theory of Markov reward processes we thus have

$$T_1^{M, N} = \lim_{k \rightarrow \infty} k^{-1} V_{M, N}^k(m, n)$$

and the limit is independent of the initial state  $(m, n)$ . It is therefore sufficient to prove that for some  $m, n \in \mathbf{N}$  and for all  $k \in \mathbf{N}$

$$V_{M, N}^k(m, n) \leq V_{M+1, N}^k(m, n)$$

For the proof of Theorem 4.2. we first state the following lemma.

LEMMA 4.3. If Assumption 4.1 holds and the conditions of (4.1) are satisfied, then for all  $k, m, n, M, N \in \mathbb{N}$ ,

$$0 \leq V_{M,N}^k(m+1, n) - V_{M,N}^k(m, n) \leq 1 \quad (4.3a)$$

$$0 \leq V_{M,N}^k(m, n) - V_{M,N}^k(m, n+1) \leq \frac{\mu_1}{\mu_2}. \quad (4.3b)$$

PROOF. (of Lemma 4.3). The proof is by induction in  $k$ . Since  $V_{M,N}^0(m, n) = 0$ , (4.3) is immediate for  $k=0$ .

Let  $k > 0$ . We have

$$\begin{aligned} & V_{M,N}^{k+1}(m+1, n) - V_{M,N}^{k+1}(m, n) \quad (4.4) \\ &= r(m+1, n) - r(m, n) \quad \circ \\ &+ \lambda_1 1_{(m < M)} [V_{M,N}^k(m+2, n) - V_{M,N}^k(m+1, n)] \\ &+ \lambda_2 1_{(n < N)} [V_{M,N}^k(m+1, n+1) - V_{M,N}^k(m, n+1)] \\ &+ \mu_1 \phi_1(m, n) [V_{M,N}^k(m, n) - V_{M,N}^k(m-1, n)] \\ &+ \mu_2 \phi_2(m+1, n) [V_{M,N}^k(m+1, n-1) - V_{M,N}^k(m, n-1)] \\ &+ [1 - \lambda_1 1_{(m < M+1)} - \lambda_2 1_{(n < N)} - \mu_1 \phi_1(m+1, n) - \mu_2 \phi_2(m, n)] [V_{M,N}^k(m+1, n) - V_{M,N}^k(m, n)] \\ &+ \mu_2 [\phi_2(m, n) - \phi_2(m+1, n)] [V_{M,N}^k(m+1, n) - V_{M,N}^k(m, n)] \\ &+ \mu_2 [\phi_2(m, n) - \phi_2(m+1, n)] [V_{M,N}^k(m, n) - V_{M,N}^k(m, n-1)] \quad \circ \end{aligned}$$

Observe that apart from the terms marked  $\circ$  all other terms consist of a difference as in (4.3a) multiplied by a constant between 0 and 1, with all multiplication constants summing up to a number smaller than or equal to one. Note also that by Assumption 4.1 and (4.3b) the second term marked  $\circ$  is negative. We will now bring the marked terms in a similar form, distinguishing between two cases.

Case  $\phi_1(m+1, n) = \phi_1(m, n)$ .

If  $\phi_1(m+1, n) = \phi_1(m, n)$  then by condition (4.1a) also  $\phi_2(m+1, n) = \phi_2(m, n)$  and by definition  $r(m+1, n) = r(m, n)$ . All  $\circ$  marked terms thus vanish, and all the remaining terms are of the form (4.3a) multiplied by a non-negative constant. Furthermore all multiplication constants sum up to  $1 - \lambda_1 1_{(m=M)}$ , which is smaller than or equal to one, thus completing the proof.

Case  $\phi_1(m+1, n) > \phi_1(m, n)$ .

Assume that  $\phi_1(m+1, n) > \phi_1(m, n)$ . The  $\circ$  marked terms in (4.4) equal

$$\begin{aligned} & r(m+1, n) - r(m, n) + \mu_2 [\phi_2(m, n) - \phi_2(m+1, n)] [V_{M,N}^k(m, n) - V_{M,N}^k(m, n-1)] \\ &= \mu_1 [\phi_1(m+1, n) - \phi_1(m, n)] \times \left\{ 1 + \frac{\mu_2 [\phi_2(m, n) - \phi_2(m+1, n)]}{\mu_1 [\phi_1(m+1, n) - \phi_1(m, n)]} [V_{M,N}^k(m, n) - V_{M,N}^k(m, n-1)] \right\} \end{aligned}$$

With (4.1a) and the induction assumption (4.3b) we have

$$0 \leq 1 + \frac{\mu_2 [\phi_2(m, n) - \phi_2(m+1, n)]}{\mu_1 [\phi_1(m+1, n) - \phi_1(m, n)]} [V_{M,N}^k(m, n) - V_{M,N}^k(m, n-1)] \leq 1 \quad (4.5)$$

so

$$\begin{aligned} & V_{M,N}^{k+1}(m+1, n) - V_{M,N}^{k+1}(m, n) \\ &= \lambda_1 1_{(m < M)} [V_{M,N}^k(m+2, n) - V_{M,N}^k(m+1, n)] \\ &+ \lambda_2 1_{(n < N)} [V_{M,N}^k(m+1, n+1) - V_{M,N}^k(m, n+1)] \\ &+ \mu_1 \phi_1(m, n) [V_{M,N}^k(m, n) - V_{M,N}^k(m-1, n)] \end{aligned}$$

$$\begin{aligned}
& + \mu_2 \phi_2(m+1, n) [V_{M,N}^k(m+1, n-1) - V_{M,N}^k(m, n-1)] \\
& + \mu_1 [\phi_1(m+1, n) - \phi_1(m, n)] [\text{TERM (4.5)}] \\
& + \mu_2 [\phi_2(m, n) - \phi_2(m+1, n)] [V_{M,N}^k(m+1, n) - V_{M,N}^k(m, n)] \\
& + [1 - \lambda_1 1_{(m < M+1)} - \lambda_2 1_{(n < N)} - \mu_1 \phi_1(m+1, n) - \mu_2 \phi_2(m, n)] [V_{M,N}^k(m+1, n) - V_{M,N}^k(m, n)],
\end{aligned}$$

where  $0 \leq \text{TERM (4.5)} \leq 1$ , thus completing the induction step. The proof of (4.3b) proceeds in an analogous way.  $\square$

With Lemma 4.3 we can proceed with the proof of Theorem 4.2.

**PROOF (of Theorem 4.2).**

Recall that we had to prove  $V_{M,N}^k(m, n) \leq V_{M+1,N}^k(m, n)$  for some  $m, n \in \mathbf{N}$  and for all  $k \in \mathbf{N}$ . In fact we will prove that this holds for all  $m, n \in \mathbf{N}$ . Again the proof is by induction in  $k$ .

$k=0$ . Trivial.

$k>0$ . Observe that

$$\begin{aligned}
& V_{M+1,N}^{k+1}(m, n) - V_{M,N}^{k+1}(m, n) \\
& = \sum_{(m', n')} p^{U^{m', n'}}((m', n'); (m, n)) V_{M+1,N}^k(m', n') - p^{U^{m, n}}((m, n); (m', n')) V_{M,N}^k(m', n') \quad (4.6) \\
& = \sum_{(m', n')} p^{U^{m, n}}((m', n'); (m, n)) [V_{M+1,N}^k(m', n') - V_{M,N}^k(m', n')] \\
& + \sum_{(m', n')} [p^{U^{m', n'}}((m', n'); (m, n)) - p^{U^{m, n}}((m, n); (m', n'))] V_{M+1,N}^k(m', n').
\end{aligned}$$

Since the first term on the right hand side of (4.6) is positive due to the induction assumption, the positivity of the second term remains to be proven. Examination of this term shows that it is equal to  $\lambda_1 [V_{M+1,N}^k(M+1, n) - V_{M+1,N}^k(M, n)]$ . With the result of Lemma 4.3 this completes the proof.  $\square$

Observe that by equation (4.6) proving the original inequality for two policies is reduced to proving an inequality for one policy. This is due to the fact that the control law for customers of type 2 is the same for both  $U^{M,N}$  and  $U^{M+1,N}$ .

By choosing the appropriate bounds as in Lemma 4.3 the method should in principle be extendable to more than two customer types. We have, however, not addressed this problem yet.

The introduction of the capacity allocation functions allows more elaborate service disciplines than the usual Processor Sharing mechanism, including those which do not lead to product-form equilibrium probabilities.

**EXAMPLE 4.4. (Monotonic Generalized Processor Sharing)**

If we choose the capacity allocation functions as in the Generalized Processor Sharing model (cf. Cohen [6], Kelly [13]), i.e.

$$\begin{aligned}
\phi_1(m, n) &= f(m+n) \frac{m}{m+n} 1_{(m+n>0)} \\
\phi_2(m, n) &= f(m+n) \frac{n}{m+n} 1_{(m+n>0)},
\end{aligned}$$

for some non-decreasing function  $f: \mathbf{N} \rightarrow \mathbf{R}_+$ , we have  $\phi_1(m, n) + \phi_2(m, n)$  equal to  $f(m+n) 1_{(m+n>0)}$ , so Assumption 4.1 and the conditions (4.1) are satisfied. Moreover, observe that for this choice the equilibrium probability distribution has a product-form (cf. [5, 13, 6]) and the throughput depends on the service time distribution only through its mean value. This leads to the following corollary.

**COROLLARY 4.5.** *If the service discipline is Monotonic Generalized Processor Sharing, then the throughput of type 1 customers is non-decreasing in the critical level of type 1 for general service time distributions.*

The following example shows that Theorem 4.2. holds also for non-standard service disciplines.

EXAMPLE 4.6. (*Priority Processor Sharing*)

Take

$$\phi_1(m, n) = 1_{(m > 0)}$$

$$\phi_2(m, n) = 1_{(m = 0)}.$$

Here we have  $\phi_1(m, n) + \phi_2(m, n) = 1$ , so conditions (4.3) are satisfied. In this example all processor capacity is awarded to type 1 customers, when they are present. This queue does not have a product-form equilibrium distribution.

We conclude this section with a theorem similar to Theorem 4.2. referring to the monotonicity of the throughput of one type of customers if the critical level of the other customer type is increased.

THEOREM 4.7. *If the conditions of (4.1) are satisfied, then  $T_2^{M, N} \geq T_2^{M+1, N}$ .*

PROOF.

Take  $r(m, n) = \mu_2 \phi_2(m, n)$ . The proof now proceeds analogously to that of Theorem 4.2.  $\square$

### 5. MONOTONICITY OF THE MEAN QUEUE LENGTH.

In this section monotonicity of the mean queue length is shown with respect to both critical levels. If either of the critical levels is increased, then the mean queue length of both types (and the total mean queue length of course) increases. The result can be stated for a rather general class of reward functions.

THEOREM 5.1. *Let  $V_{M, N}^k(m, n)$  be the expected total reward over  $k$  steps, when starting in state  $(m, n)$  and using policy  $U^{M, N}$ . If the reward function  $r(m, n)$  is non-decreasing in both its arguments  $m$  and  $n$  and Assumption 4.1 holds, then  $V_{M, N}^k(m, n) \leq V_{M+1, N}^k(m, n)$  for all  $m, n \in \mathbf{N}$ .*

PROOF.

Analogously to the proof of Theorem 4.2. it is sufficient to prove that

$$0 \leq V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n), \quad m, n, M, N \in \mathbf{N}.$$

LEMMA 5.2. *If  $r(m, n)$  is non-decreasing in both arguments, then*

$$0 \leq V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n) \tag{5.1a}$$

$$0 \leq V_{M, N}^k(m, n+1) - V_{M, N}^k(m, n), \tag{5.1b}$$

for all  $m, n \in \mathbf{N}$ .

PROOF.

Again the proof is by induction in  $k$ . The case  $k=0$  is trivial. For  $k>0$  write  $V_{M, N}^{k+1}(m+1, n) - V_{M, N}^{k+1}(m, n)$  as

$$\begin{aligned} & V_{M, N}^{k+1}(m+1, n) - V_{M, N}^{k+1}(m, n) \tag{5.2} \\ &= r(m+1, n) - r(m, n) \\ &+ \lambda_1 1_{(m < M)} [V_{M, N}^k(m+2, n) - V_{M, N}^k(m+1, n)] \\ &+ \lambda_2 1_{(n < N)} [V_{M, N}^k(m+1, n+1) - V_{M, N}^k(m, n+1)] \\ &+ \mu_1 \phi_1(m, n) [V_{M, N}^k(m, n) - V_{M, N}^k(m-1, n)] \end{aligned}$$

$$\begin{aligned}
& + \mu_2 \phi_2(m+1, n) [V_{M, N}^k(m+1, n-1) - V_{M, N}^k(m, n-1)] \\
& + [1 - \lambda_1 1_{(m < M+1)} - \lambda_2 1_{(n < N)} - \mu_1 \phi_1(m+1, n) - \mu_2 \phi_2(m, n)] [V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n)] \\
& + \mu_2 [\phi_2(m, n) - \phi_2(m+1, n)] [V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n-1)].
\end{aligned}$$

Since  $\phi_2(m, n)$  is non-increasing in  $m$  due to Assumption 4.1 and  $V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n-1) = V_{M, N}^k(m+1, n) - V_{M, N}^k(m, n) + V_{M, N}^k(m, n) - V_{M, N}^k(m, n-1) \geq 0$ , all terms in (5.2) are non-negative, thus completing the induction step. The proof of (5.1b) is analogous.  $\square$

Note that if the reward function  $r$  is positive and non-decreasing in both arguments, then also  $r^i$  has these properties for all  $i \in \mathbb{N}$ . The monotonicity of  $r$  thus makes the higher moments also monotonic in the critical levels.

#### EXAMPLE 5.3.

If we take  $r(m, n) = m$  or  $r(m, n) = m + n$ , we see that both the mean queue length of type 1 and the total mean queue length are non-decreasing if the critical level of type 1 is increased. Due to the symmetry of Theorem 5.1 this also holds if the critical level of type 2 is increased.

#### 6. MONOTONICITY OF THE MEAN SOJOURN TIME.

With Little's result we can combine Theorems 4.2. and 5.1. to show that the mean sojourn time of type 2 customers increases if the critical level for type 1 customers increases. Unfortunately this technique can not be used for the monotonicity of type 1 customers. In this section we show that this performance measure is also non-decreasing if either of the critical levels is increased. We have been able to prove this only for standard Processor Sharing, however. The capacity allocation functions  $\phi_1$  and  $\phi_2$  thus are chosen as

$$\begin{aligned}
\phi_1(m, n) &= \frac{m}{m+n} 1_{(m+n > 0)} \\
\phi_2(m, n) &= \frac{n}{m+n} 1_{(m+n > 0)}.
\end{aligned}$$

The proof of the theorem relies on the product-form of the equilibrium distribution and the closed form formula of the mean sojourn time that can be derived from this distribution. It is well known (cf. Baskett *et al.* [5] and Kelly [13]), that the equilibrium probability of the population vector  $(m, n)$  under the policy  $U^{M, N}$ , is equal to

$$p^{M, N}(m, n) = C[M, N] p(m, n), \quad 0 \leq m \leq M, \quad 0 \leq n \leq N, \quad (6.1)$$

where

$$p(m, n) = \binom{m+n}{n} \rho_1^m \rho_2^n, \quad 0 \leq m \leq M, \quad 0 \leq n \leq N, \quad (6.2)$$

$$C[M, N] = \left[ \sum_{m=0}^M \sum_{n=0}^N \binom{m+n}{n} \rho_1^m \rho_2^n \right]^{-1}. \quad (6.3)$$

With Little's formula the mean sojourn time of type 1 customers is equal to

$$S_1^{M, N} = \frac{A(M)}{B(M)} \quad (6.4)$$

where

$$A(M) = \sum_{m=0}^M \sum_{n=0}^N m p(m, n) \quad (6.5)$$

$$B(M) = \sum_{m=0}^M \sum_{n=0}^N \frac{m}{m+n} \mu_1 p(m, n). \quad (6.6)$$

The variable  $N$  is suppressed in the notation of  $A$  and  $B$ , since  $N$  is held constant throughout this section.

**THEOREM 6.1.** *If  $\rho_2 > 0$ , then*

$$S_1^{M,N} < S_1^{M+1,N} \quad (6.7)$$

Again we need a preliminary lemma for the proof of the theorem.

**LEMMA 6.2.** *If  $A, B : \mathbf{N} \rightarrow \mathbf{R}_+$ , increasing,  $A(0) = B(0) = 0$  and*

$$\frac{\Delta A(M+1)}{\Delta B(M+1)} > \frac{\Delta A(M)}{\Delta B(M)}, \quad M \geq 1, \quad (6.8)$$

where  $\Delta A(M) := A(M) - A(M-1)$  and  $\Delta B(M)$  analogously, then

$$\frac{A(M+1)}{B(M+1)} > \frac{A(M)}{B(M)}, \quad M \geq 1. \quad (6.9)$$

**PROOF** (by induction).

( $M = 1$ ). First note that for positive  $a, b, c, d$ :

$$\frac{a+c}{b+d} > \frac{a}{b} \Leftrightarrow \frac{c}{d} > \frac{a}{b} \Leftrightarrow \frac{c}{d} > \frac{a+c}{b+d}. \quad (6.10)$$

Equation (6.9) reads for  $M = 1$

$$\frac{A(2)}{B(2)} > \frac{A(1)}{B(1)}$$

Note that since  $A(0) = B(0) = 0$ , by definition  $A(1) = \Delta A(1)$ ,  $B(1) = \Delta B(1)$ . Since  $A(2) = A(1) + \Delta A(2)$  and  $B(2) = B(1) + \Delta B(2)$ , this holds if and only if

$$\begin{aligned} \frac{A(1) + \Delta A(2)}{B(1) + \Delta B(2)} &> \frac{A(1)}{B(1)} \\ \Leftrightarrow \frac{\Delta A(2)}{\Delta B(2)} &> \frac{A(1)}{B(1)}. \end{aligned}$$

The latter inequality is true by  $A(1) = \Delta A(1)$  and  $B(1) = \Delta B(1)$ .

( $M > 0$ ). Assume that we have

$$\frac{A(m+1)}{B(m+1)} > \frac{A(m)}{B(m)}, \quad m = 1, \dots, M-1.$$

For  $m = M-1$  this yields

$$\begin{aligned} \frac{A(M)}{B(M)} &> \frac{A(M-1)}{B(M-1)} \\ \Leftrightarrow \frac{A(M-1) + \Delta A(M)}{B(M-1) + \Delta B(M)} &> \frac{A(M-1)}{B(M-1)} \\ \Leftrightarrow \frac{\Delta A(M)}{\Delta B(M)} &> \frac{A(M-1) + \Delta A(M)}{B(M-1) + \Delta B(M)} \\ \Leftrightarrow \frac{\Delta A(M)}{\Delta B(M)} &> \frac{A(M)}{B(M)}, \end{aligned}$$

where the second equivalence is an application of (6.10). With the condition (6.8) this implies

$$\begin{aligned} \frac{\Delta A(M+1)}{\Delta B(M+1)} &> \frac{A(M)}{B(M)} \\ \Leftrightarrow \frac{A(M) + \Delta A(M+1)}{B(M) + \Delta B(M+1)} &> \frac{A(M)}{B(M)} \end{aligned}$$

$$\Leftrightarrow \frac{A(M+1)}{B(M+1)} > \frac{A(M)}{B(M)} \quad \square$$

PROOF (of Theorem 6.1.).

According to Lemma 6.2. it is sufficient to prove that

$$\frac{\Delta A(M+1)}{\Delta B(M+1)} > \frac{\Delta A(M)}{\Delta B(M)}.$$

This is equivalent to

$$\frac{\sum_{n=0}^N \binom{M+1+n}{n} \rho_2^n}{\sum_{n=0}^N \frac{1}{M+1+n} \binom{M+1+n}{n} \rho_2^n} > \frac{\sum_{n=0}^N \binom{M+n}{n} \rho_2^n}{\sum_{n=0}^N \frac{1}{M+n} \binom{M+n}{n} \rho_2^n}. \quad (6.11)$$

If we define  $w_n = (M+n)! \rho_2^n / n!$ , then (6.11) reads

$$\left[ \sum_{n=0}^N (M+n+1) w_n \right] \left[ \sum_{n=0}^N \frac{1}{M+n} w_n \right] > \left[ \sum_{n=0}^N w_n \right]^2 \quad (6.12)$$

$$\Leftrightarrow \sum_{n=0}^N \frac{M+n+1}{M+n} w_n^2 + \sum_{i < j} \left[ \frac{M+i+1}{M+j} + \frac{M+j+1}{M+i} \right] w_i w_j > \sum_{n=0}^N w_n^2 + 2 \sum_{i < j} w_i w_j \quad (6.13)$$

The first terms on both sides of (6.13) can be compared immediately and yield the desired inequality. The second terms also satisfy this inequality since

$$\begin{aligned} & (M+i+1)(M+i) + (M+j+1)(M+j) \geq 2(M+i)(M+j) \\ \Leftrightarrow & (M+i)(i-j+1) + (M+j)(j-i+1) > 0 \\ \Leftrightarrow & (i-j)^2 + 2M + i + j > 0 \quad \square \end{aligned}$$

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